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| **Project1 Details** |

Implement a MIPS simulator that can perform the following steps:

* load a specified MIPS text file.
* generate the assembly code.
* perform instruction-by-instruction simulation of the generated assembly.

Detailed Instructions are available here: [Project1 (PDF)](http://www.cise.ufl.edu/class/cda5155sp12/projects/Project1/project1.pdf).   
  
The MIPS Instruction-Set manual is available here: [MIPS Instruction Set (PDF)](http://www.cise.ufl.edu/class/cda5155sp12/projects/mips.pdf).   
  
The sample input/output files are given below:

* [sample.txt](http://www.cise.ufl.edu/class/cda5155sp12/projects/Project1/sample.txt): This is the input to your program.
* [disassembly.txt](http://www.cise.ufl.edu/class/cda5155sp12/projects/Project1/disassembly.txt): This is what your program should produce as disassembled output.
* [simulation.txt](http://www.cise.ufl.edu/class/cda5155sp12/projects/Project1/simulation.txt): This is what your program should output as simulation trace.

The internal test cases used for grading:

* [sample\_int.txt](http://www.cise.ufl.edu/class/cda5155sp12/projects/Project1/sample_int.txt): This is the input to your program.
* [disassembly\_int.txt](http://www.cise.ufl.edu/class/cda5155sp12/projects/Project1/disassembly_int.txt): This is what your program should produce as disassembled output.
* [simulation\_int.txt](http://www.cise.ufl.edu/class/cda5155sp12/projects/Project1/simulation_int.txt): This is what your program should output as simulation trace.